

Claim 3, further comprising:

a first unit including said synchronization information detection circuit and said data reproduction circuit; and

a second unit including said block end position calculation circuit and said data processing circuit;

wherein said synchronization information detection signal is sent from said first unit to said second unit and said data is sent from said first unit to said second unit after said synchronization information detection signal has been sent.

5. A data reproduction apparatus according to Claim 3, further comprising:

a first unit including said synchronization information detection circuit and said data reproduction circuit; and

a second unit including said block end position calculation circuit and said data processing circuit;

wherein said synchronization information detection signal and said data are sent from said first unit to said second unit and said calculated block end position is notified from said second unit to said first unit.

6. A data reproduction apparatus according to Claim 4, wherein said synchronization information detection signal and said data are sent from said first

7. A data reproduction apparatus according to Claim 3, further comprising:

a first unit including said synchronization information detection circuit, said data reproduction circuit and said data range prescribing circuit; and

wherein said synchronization information detection signal is sent from said first unit to said second unit and said data and said data settling signal are sent from said first unit to said second unit after said synchronization information detection signal has

been sent.

8. A data reproduction apparatus comprising:
a format control circuit for activating a
reading command signal for commanding to read a block
to be reproduced on a recording medium in which a
plurality of blocks are recorded as a block unit having
a data string including:

bit synchronization information for perform-
ing bit synchronization;

symbol synchronization information for
performing symbol synchronization;

data;

correction information for correcting said
data; and

additional area;

and calculating an end position of said block on said
recording medium on the basis of a synchronization
information detection signal to inactivate said reading
command signal;

a synchronization information detection
circuit for reading a block on said recording medium
during a period that said reading command signal is
active and detecting said symbol synchronization
information to produce said synchronization information
detection signal;

a data reproduction circuit for reproducing
said data and said correction information of said block
read out from said recording medium during the period

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a data processing circuit for processing said data and said correction information produced from said data reproduction circuit after elapse of a reproduction delay time on the basis of said synchronization information signal.

- a code reproduction circuit for reproducing a
code string on the basis of said synchronization
information detection signal to be produced; and

wherein after said synchronization information detection signal is produced, said code string is processed by said decoding circuit.

10. A data reproduction apparatus comprising:
a data string production circuit for produc-
ing a series of data string having synchronization

information added thereto;

a recording circuit for recording said data string in a recording medium as a record data string; and

a control circuit for controlling said data string production circuit and said recording circuit independently;

wherein said control circuit controls said recording circuit and produces said record data string after said data string of said data string production circuit is produced.

11. A data reproduction apparatus according to Claim 10, further comprising:

a first unit including said data string production circuit and said control circuit; and

a second unit including said recording circuit;

wherein said data string and a control signal for controlling said recording circuit by means of said control circuit are sent from said first unit to said second unit and said data string is produced in advance of said control signal.

12. A data reproduction apparatus comprising:

a data production circuit for sending out data;

a coding circuit for coding said data;

a data string production circuit for adding synchronization information to said coded data to

- 56 -

produce a data string;

 a recording circuit for recording said data string as a record data string; and

 a format control circuit for controlling said data string production circuit and said recording circuit independently;

 wherein said control circuit controls said recording circuit and produces said record data string after when said data string of said data string production circuit is produced.

- 56 -

produce a data string;

 a recording circuit for recording said data string as a record data string; and

 a format control circuit for controlling said data string production circuit and said recording circuit independently;

 wherein said control circuit controls said recording circuit and produces said record data string after when said data string of said data string production circuit is produced.

- 56 -

produce a data string;

 a recording circuit for recording said data string as a record data string; and

 a format control circuit for controlling said data string production circuit and said recording circuit independently;

 wherein said control circuit controls said recording circuit and produces said record data string after when said data string of said data string production circuit is produced.

- 56 -

produce a data string;

 a recording circuit for recording said data string as a record data string; and

 a format control circuit for controlling said data string production circuit and said recording circuit independently;

 wherein said control circuit controls said recording circuit and produces said record data string after when said data string of said data string production circuit is produced.